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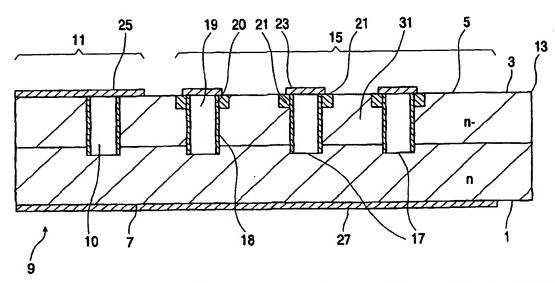
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(54) Title: SEMICONDUCTOR DEVICE WITH EDGE STRUCTURE



(57) Abstract: A semiconductor device has an edge termination region (15) having a plurality of trenches (17). Conductive material (20) and insulating material (19) is formed at the trenches, and surface implants (21) are formed on either side of the trenches. A conductive bridge (23) connects the surface implants (21) to allow equilibrium to be reached in reverse bias.



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Minimum documentation searched (classification system followed by classification symbols) IPC $\,7\,$ H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

EPO-Internal, INSPEC, PAJ

c. Docom	ENTS CONSIDERED TO BE RELEVANT		
Category °	Citation of document, with indication, where appropriate, of ti	he relevant passages	Relevant to claim No.
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A	EP 1 011 146 A (ST MICROELECTE 21 June 2000 (2000-06-21) cited in the application column 5, line 54 - line 57;		1,2,5, 8-10
X Fur	ther documents are listed in the continuation of box C.	Patent family members are listed	in annex.
A docum		 *T* later document published after the into or priority date and not in conflict with cited to understand the principle or the invention *X* document of particular relevance; the cannot be considered novel or cannot involve an inventive step when the determined the confliction of /li>	the application but ecory underlying the claimed invention to considered to coument is taken alone claimed invention
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C.(Continu	ation) DOCUMENTS CONSIDERED TO BE RELEVANT	Relevant to claim No.
Category °	Citation of document, with Indication, where appropriate, of the relevant passages	Helevani to claim no.
A	VAN DALEN R ET AL: "BREAKING THE SILICON LIMIT USING SEMI-INSULATING RESURF LAYERS" PROCEEDINGS OF THE 13TH INTERNATIONAL SYMPOSIUM ON POWER SEMICONDUCTOR DEVICES & ICS (ISPSD '01), OSAKA, JP, 4 - 7 June 2001, pages 391-394, XP001052044 IEEE, NEW YORK, NY, USA ISBN: 4-88686-056-7C figures 1B,1C	1,2,5,6, 9,10
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